3 - 000/frain/200		Class Subclass		U.S. UTII		P.E.	Application PATE	NT DATE	PATENT NUMBER
Ŀ	Robert	18 No.	D (n 2 f)	CLASS 711	SUB	class 5	2186	EXAMINER Farkay	
Robert Morman Virod Lakhani Memory system having serial selection of memory devices a									s and method
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TERMINAL		DRAWINGS	ĺ	CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent		1		NOTICE OF AL	LOWANCE MAILED	
ubsequent to (date) as been disclaimed.	(Assistant	Examiner)	(Date)	_	•	
The term of this patent shall not extend beyond the expiration date						
of U.S Patent. No.			i	ISS	UE FEE	
				Amount Due	Date Paid	
	(Primary	Examiner)	(Date)			
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this patent have been disclaimed.	(Legal Instruments Examiner) (Date)					

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